

Rajasekar Venkatesan, Ph.D.

CONTACT INFORMATION	Scientist Institute of Infocomm Research, Agency for Science, Technology & Research, #21-01, Connexis (South Tower) 1 Fusionopolis Way, S138632	<i>Handphone:</i> +65 9772 0584 <i>E-mail:</i> raja0046@e.ntu.edu.sg <i>GitHub:</i> github.com/rajasekar-venkatesan <i>LinkedIn:</i> sg.linkedin.com/in/rajasekar-v <i>Webpage:</i> rajasekarv.wixsite.com/rajasekar-venkatesan
RESUME SUMMARY	Data Scientist with 5+ years of experience in the field of <i>machine learning</i> . In-depth knowledge and research experience in <i>Neural Networks, Deep Learning, Natural Language Processing, Image Classification and Captioning, Machine Learning Techniques</i> and <i>Data Mining</i> .	
EDUCATION	Nanyang Technological University (NTU) , Singapore <i>Ph.D., Machine Learning</i> <ul style="list-style-type: none">• Thesis Title: Human-inspired Progressive Learning Technique for Classification Problems• Advisor: Prof. Er Meng Joo PSG College of Technology , Coimbatore, Tamil Nadu, India B.Eng, Electronics and Communication Engineering	August 2012 - August 2016 CGPA: 4.75/5.0 July 2008 - May 2012 CGPA: 9.82/10.0
SKILLS	Languages <i>Programming Languages</i> <i>Scripting Languages</i> <i>Hardware Description Languages</i> Machine Learning Version Control Systems Processor Architectures Operating System <i>Server</i> <i>RTOS</i>	Python, R, Matlab, C, C++ Frequently used Python libraries: <i>Machine Learning:</i> scikit-learn, pandas, numpy and scipy <i>Deep Learning:</i> tensorflow, pyTorch, keras <i>NLP:</i> gensim, NLTK, textblob Shell, Python, Perl VHDL, Verilog <i>New Contribution:</i> Progressive learning, Label-independent (aka universal) classifier <i>Deep Learning:</i> Image classification using CNN, Time series prediction using LSTM, RNN <i>Supervised Learning:</i> Classification (SVM, DT, RF, Logistic Regression, MLP) and Regression (SVR, Linear and Polynomial Regression) <i>Unsupervised learning:</i> Clustering (k means clustering and hierarchical clustering) <i>Reinforcement Learning:</i> UCB and Thompson Sampling <i>Association Rule Learning:</i> Apriori and eclat for learning association rules <i>Dimensionality Reduction:</i> PCA and LDA Git, CVS ARM, 5604B (32 bit C), 5416 (16 bit DSP Processor), S12X (16 bit C), 8051(8 bit C), 8085(8 bit P) Linux(Distros: Ubuntu, CentOS, Fedora), Windows μ COS II, QNX
THESIS SUMMARY	<i>Human-inspired Progressive Learning Technique for Classification Problems</i> <ul style="list-style-type: none">• Development of human-inspired machine learning techniques for <i>classification</i> problems• Neural Network based architecture for high speed multi-class and multi-label classification• Introduced and developed the <i>progressive learning technique (PLT)</i> and incorporated <i>PLT</i> on Neural Network based online multi-class and multi-label classifiers• Developed a <i>universal classifier</i> that can be used for binary, multi-class and multi-label classification problems using Single hidden layer feedforward neural network	
WORK EXPERIENCE	Institute of Infocomm Research, A*Star , Singapore <i>Scientist</i> Development of machine learning techniques for cybersecurity applications Nanyang Technological University , Singapore <i>Researcher</i>	August 2017 - Present September 2016 - July 2017 <ul style="list-style-type: none">• Time series prediction using deep architectures, online learning of high speed streaming data• Received appreciation from Rolls Royce Technical Team in UK for achieving significant results using the developed deep architecture

Juniper Networks, India

Software Engineering Intern

January 2012 - May 2012

- Developed intelligent automated tools for Network Address Translation(NAT) and various NAT types
- Received commendation for excellence from Director of the business unit, Juniper Networks Headquarters, Sunnyvale, USA

TEACHING
EXPERIENCE

Nanyang Technological University, Singapore

- Completed a certified course on University Training for Teaching Assistance by NTU
- Handled tutorial classes and laboratory classes for undergraduate students in NTU
- Closely worked with supervisor in writing research grants and developing course modules
- Mentored 6 undergraduate students in their FYPs (Final Year Project), 4 overseas exchange students and 4 MEng students

SELECTED
PUBLICATIONS*

R. Venkatesan, M.J. Er, "A Novel Progressive Learning Technique for Multi-class Classification", *Neuro-computing*, vol. 207, pp. 310-321.

R. Venkatesan, M.J. Er, M. Dave, M. Prathama, S. Wu, "A Novel Online Multi-label Classifier for High-speed Streaming Data Applications", *Evolving Systems*, 2016, pp.1-13.

R. Venkatesan, M.J. Er, S. Wu, M. Prathama, "A Novel Online Real-time Classifier for Multi-label Data Streams", *International Joint Conference on Neural Networks*, Vancouver, Canada, 2016, pp. 1833-1840.

R. Venkatesan, M.J. Er, "Multi-label Classification Based on Extreme Learning Machines", *International Conference on Control, Automation, Robotics and Vision*, pp. 619-624.

R. Venkatesan, M.J. Er, Ning Wang, Chiang-Ju Chien, "Progressive Learning Strategies for Multi-class Classification", *International Automatic Control Conference (Accepted for Presentation)*

M.J. Er, **R. Venkatesan**, N. Wang, "A High Speed Multi-label Classifier Based on Extreme Learning Machines", *International Conference on Extreme Learning Machines*, vol. 2, pp. 437-454.

M.J. Er, **R. Venkatesan**, N. Wang "An Online Universal Classifier for Binary, Multi-class and Multi-label Classification", *IEEE Conference on System, Man and Cybernetics*, 2016, To be published.

M.J. Er, **R. Venkatesan**, "A Survey of Cognitive Architecture for Cognitive Robotics", *Book Chapter, Data Mining and Knowledge Discovery*, Under Review.

A. Narayanan, M. Chandramohan, **R. Venkatesan**, L. Chen, Y. Liu, S. Jaiswal, "graph2vec: Learning Distributed Representations of Graphs", *In Workshop on Mining and Learning with Graphs (co-located with KDD)*, 2017

M.J. Er, V.K. Yalavarathi, N. Wang, **R. Venkatesan**, "A Novel Incremental Class Learning Technique for Multi-class Classification", *Advances in Neural Networks, ISNN2016*, pp. 474-481.

M. Dave, S. Tapiawala, M.J. Er, **R. Venkatesan**, "A Novel Progressive Multi-label Classifier for Class-incremental Data", *IEEE Conference on System, Man and Cybernetics*, 2016, To be published.

Y. Zhang, M.J. Er, **R. Venkatesan**, N. Wang, M. Pratama, "Sentiment Classification Using Comprehensive Attention Recurrent Models", *International Joint Conference on Neural Networks*, Vancouver, Canada, 2016, pp. 1562-1569.

* *Complete list of publications is available in <http://rajasekar.vixsite.com/rajasekar-venkatesan/publications>*

AWARDS AND
SCHOLARSHIPS

Awarded *NTU Research Scholarship* for four consecutive years, 2012-2016.

Awarded a *gold medal* by PSG College of Technology for being an outstanding student during undergraduate studies.

Awarded *best all-rounder award* by PSG College of Technology for excelling in academic, co-curricular and extra-curricular activities during my undergraduate studies.

Awarded *Central Government of Indias outstanding students scholarship* called Central Sector Scholarship for four consecutive years 2008-2012.

Won *second place* in self-guided car race, a Smart Car race competition in KRIYA11 conducted by Freescale Semiconductors in PSG College of Technology

REFERENCES

Er Meng Joo

Professor

School of Electrical and Electronic Engineering

Nanyang Technological University

Singapore 639798

Email: emjer@ntu.edu.sg

Mahardhika Pratama

Assistant Professor

School of Computer Science and Engineering

Nanyang Technological University

Singapore 639798

Email: mpratama@ntu.edu.sg